PicoCOM1.2 GPIO Reference Card V1.2 09.12.2024

Pin layout for Board Rev. 1.0x

J1	Function	Device	GPIO	Mode	/sys/class/gpio/gpio#	PC1-SINTF
1	ETH_A_TX-	ETH	-	-	-	(J2_1/2)
2	ETH_A_RX-	ETH	-	-	-	(J2_3/6)
3	ETH_A_TX+	ETH	-	-	-	(J2_1/2)
4	ETH_A_RX+	ETH	-	-	-	(J2_3/6)
5	V33					
6	V33					
7	GND					
8	GND					
9	VBAT					
10	/RESET	BOOT	-	-	-	(S1)
11	I2C_B_SCL	I2C	GPIO1_IO28	IO	28	J11_23
12	I2C_B_SDA	I2C	GPIO1_IO29	IO	29	J11_24
13	UART_B_TXD	UART	GPIO1_IO20	IO	20	(J6_5)
14	UART_B_RXD	UART	GPIO1_IO21	IO	21	(J6_3)
15	UART_B_RTS	UART	GPIO1_IO22	IO	22	(J6_4)
16	UART_B_CTS	UART	GPIO1_IO23	IO	23	(J6_6)
17	UART_A_TXD	UART	GPIO1_IO24	IO	24	(J8_3/5)
18	UART_A_RXD	UART	GPIO1_IO25	IO	25	(J8_3/5)
19	USBH_A_DP	USB HOST	-	-	-	J4_3
20	USBH_A_DN	USB HOST	-	-	-	J4_2
21	USB_OTG_DP	USB OTG	-	-	-	J3_3
22	USB_OTG_DN	USB OTG	-	-	-	J3_2
23	USB_OTG_VBUS	USB OTG	-	-	-	J3_1
24	USBH_A_PWR	USB HOST	GPIO2_IO12	IO	44	(J4_1)
25	GND					
26	GPIO1	GPIO	GPIO3_IO17	IO	81	J12_9
27	GPIO2	GPIO	GPIO3_IO18	IO	82	J12_7
28	GPIO3	GPIO	GPIO3_IO19	IO	83	J12_1
29	USB_OTG_PWR	USB OTG	GPIO2_IO08	IO	40	J12_3
30	GPIO4		GPIO3_IO20	IO	84	J12_5
31	BOOTSEL	BOOT	-	-	-	J13_1
32	I2C_A_SDA	I2C	GPIO1_IO31	IO	31	J11_21
33	I2C_A_SCL	I2C	GPIO1_IO30	IO	30	J11_22
34	SD_A_DAT0	SDMMC	GPIO3_IO25	IO	89	J5_7
35	SD_A_DAT1	SDMMC	GPIO3_IO26	IO	90	J5_8
36	SD_A_DAT2	SDMMC	GPIO3_IO27	IO	91	J5_9
37	SD_A_DAT3	SDMMC	GPIO3_IO28	IO	92	J5_1
38	SD_A_CLK	SDMMC	GPIO3_IO24	IO	88	J5_5
39	SD_A_CMD	SDMMC	GPIO3_IO23	IO	87	J5_2
40	UART_C_CTS / SD_A_CD	UART / SDMMC	GPIO1_IO19	IO	19	J11_6
41	UART_C_RTS / SD_A_WP	UART / SDMMC	GPIO1_IO18	IO	18	
42	GND					
43	UART_C_TXD	UART	GPIO1_IO16	IO	16	(J7_5)
44	UART_C_RXD	UART	GPIO1_IO17	IO	17	(J7_3)
45	UART_D_TXD	UART	GPIO3_IO00	IO	64	(J6_2)
46	UART_D_RXD	UART	GPIO3_IO01	IO	65	J11_4
47	UART_D_RTS	UART	GPIO3_IO02	IO	66	(J6_7)
48	UART_D_CTS	UART	GPIO3_IO03	IO	67	(J6_8)
49	UART_A_RTS	UART	GPIO1_IO26	IO	26	(J8_3/5)
50	UART_A_CTS	UART	GPIO1_IO27	IO	27	J11_5
51	CAN_A_TX	CAN	GPIO3_IO13	IO	77	(J9_3/4)
52	CAN_A_RX	CAN	GPIO3_IO14	IO	78	(J9_3/4)
53	CAN_B_TX	CAN	GPIO3_IO15	IO	79	J11_25
54	CAN_B_RX	CAN	GPIO3_IO16	IO	80	J11_26
55	SPI_A_MISO	SPI	GPIO4_IO28	IO	124	J11_15
56	SPI_A_MOSI	SPI	GPIO4_IO27	IO	123	J11_16
57	SPI_A_SCLK	SPI	GPIO4_IO25	IO	121	J11_17
58	SPI_A_CS0	SPI	GPIO4_IO26	IO	122	J11_18
59	SPI_A_CS1	SPI	GPIO3_IO10	IO	74	J11_19
60	SPI_A_CS2	SPI	GPIO3_IO11	IO	75	J11_20
61	GND					
62	GND					
63	SPI_B_SCLK / UART_F_TXD	SPI / UART	GPIO4_IO21	IO	117	J11_7
64	SPI_B_CS0 / UART_F_RXD	SPI / UART	GPIO4_IO22	IO	118	J11_8
65	SPI_B_MISO / UART_F_RTS	SPI / UART	GPIO4_IO24	IO	120	J11_9
66	SPI B_MOSI / UART_F_CTS	SPI / UART	GPIO4_IO23	IO	119	J11_10
67	UART_E_TXD	UART	GPIO3_IO21	IO	85	J11_11
68	UART_E_RXD	UART	GPIO3_IO22	IO	86	J11_12
69	GPIO5	GPIO	GPIO3_IO12	IO	76	J11_13
70	PWM_D	PWM	GPIO1_IO04	IO	4	J11_14
71	ETH_A_LED	ETH	-	-	-	
72	GND					
73	GND					
74	ADC_A / PWM_A	ADC / PWM	GPIO1_IO08	IO	8	J11_1
75	ADC_B / PWM_B	ADC / PWM	GPIO1_IO05	IO	5	J11_2
76	ADC_C / PWM_C	ADC / PWM	GPIO1_IO09	IO	9	J11_3
77	AUDIO_LOUT	AUDIO	-	-	-	J10_3
78	AUDIO_ROUT	AUDIO	-	-	-	J10_4
79	AUDIO_LIN	AUDIO	-	-	-	J10_5
80	AUDIO_RIN	AUDIO	-	-	-	J10_6

Remark

The GPIO Reference Card is a software development tool. It just lists the numbers needed for accessing GPIO ports in Linux.

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Please refer to the PicoCOM1.2 Hardware Documentation for hardware development.